




JSS Mahavidyapeetha
JSS Academy of Technical Education, Noida
Department of Electronics & Communication Engineering.



FACULTY PROFILE

1. Personal Details

NAME	Mr. YATHEESH.K.C.	
DEPARTMENT	ELECTRONICS & COMMUNICATION ENGINEERING	
DESIGNATION	ASSISTANT PROFESSOR	
PHONE	9810804569	
EMAIL ID	yatheeshkc@jssaten.ac.in	
Date of Joining (JSSATEN)	4-08-2001	

2. Experience

Total Experience in Years: 22	Teaching: 20 Years	Industry: 2 Years	Research: 3 Years
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3. Qualifications

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
B.E.	Electronics and Instrumentation	1996	SIT, Tumkur	Bangalore University
M.Tech.	VLSI Design	2009	UP Tech. University, Lucknow	UP Tech. University, Lucknow
Ph.D.	VLSI Design	Pursuing	KSIT, Bengaluru	VTU, Belgavi
Post Doc.	-	-	-	-

4. Research & Publications

Papers Published in Web of Science indexed Journals	International: 00	National: 00
Papers Published in SCOPUS indexed Journals	International: 01	National: 00

Papers Published in other Journals	International: 00	National: 00
Papers Presented in Conferences / Symposium	International: 03	National: 00
Books / Book chapters Published	Name of the book: NIL Publisher: Year of Publication:	

5. Research Guidance

PhD Guide? Give field & University	No	NA
Ph.D.s / Projects Guided	Ph.D. Awarded: No Guiding: NIL	Projects at Master's Level: - Nil Projects at Bachelor's Level: 20

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	-	-	-	-

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
-	-	-

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	-	-	-	-

7. Awards Received

Awards "Best Paper Award"	Nil
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8. Publications

i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	An innovative design of spin transfer torque based ternary content addressable memory with match line sense amplifier	K. C. Yatheesh1 • M. J. Shanti Prasad2	Analog Integrated Circuits and Signal Processing	Volume No. Issue No. Year 107, 2nd January 2020	WOS and Scopus	1.35	Springer

ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
-	-	-	-	-	-	-	-

iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	A Novel Transition Metal Dichalcogenide based Spin Transfer Torque Ternary Content Addressable Memory Design for Match Line Sense Amplifier	Yatheesh K. C.	IEEE CONECCT 2020	2020	Scopus	0.65	IEEE
2	FPGA Implementation	Yatheesh K. C.	International Conference on	2020			JSSATE

	of Multipumping Enabled with Multiported SRAM using Ternary Content Addressable Memory		Intelligent Control and Computation for Smart Energy and Mechatronic Systems				
3	Advanced Gas Leakage Detection System Using Acoustic Sensors	Anushree Tiwari, Yatheesh K. C.	International conference on computing, power and communication technologies 2018 (gucon)	2018	Scopus	0.65	IEEE

iv. Workshops /Conferences Attended

Sl. No.	Name of the workshop / Conference	Organizer	Date
1	INTERNATIONAL CONFERENCE ON COMPUTING, POWER AND COMMUNICATION TECHNOLOGIES 2018 (GUCON)	IEEE	Sep 28-29, 2018

v. Workshops / Conference (Organized)

Sl. No.	Name of the workshop / Conference	Organized by	Date	Role
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vi. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

Sl. No.	Name of the workshop / Conference	Organizer	Date
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9. Details of NPTEL / COURSERA courses completed

Sl. No.	Name of the subject	Organized by	Date of completion / Award	Grade / Marks
1	Signals and Systems	NPTEL	April 2020	71%

10. Membership of Professional Bodies:

Nil

11. Any other information you will like to share about your professional experience

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